



ATTORNEY DOCKET NO. TU9-98-010

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Serial No. 09/131,846

Filed: July 24, 1998

**For: DATA PROCESSING METHOD AND
SYSTEM FOR SIMULATION OF
HARDWARE FAULTS UTILIZING A
PCI BUS**

Examiner: BRYCE P. BONZO

Group Art Unit: 2184

RECEIVED

MAY 15 2001

Technology Center 2100

RESPONSE "B" UNDER 37 C.F.R. § 1.112

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This Response is submitted in answer to the Examiner's Action, dated March 2, 2001, having a shortened statutory period set to expire June 2, 2001.

CERTIFICATE OF MAILING

37 C.F.R. § 1.8(A)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Assistant Commissioner of Patents and Trademarks, Box Responses No Fee, Washington, D. C. 20231.

5/2/2001

Date _____

Signature

Denal Faris